CALL FOR TUTORIALS

The IEEE International Symposium on Circuits and Systems (ISCAS) is the flagship conference of the IEEE Circuits and Systems (CAS) Society and the world’s premiere forum for researchers in the active fields of theory, design and implementation of circuits and systems. This is accomplished through technical conference sessions, poster sessions, live demonstration sessions, and publication of conference papers. ISCAS 2023 will be driven by the theme “Technology Disruption and Society” aiming to emphasize the potential of the CAS Society to find innovative solutions to challenges facing society today.

Participation in tutorials and technical meetings is an excellent opportunity for lifelong learning, and ISCAS 2023 is committed to supporting your professional development. Continuing Education Units (CEUs) will be available for ISCAS delegates participating in tutorials or attending technical sessions.

Suitable tutorial topics range from emerging research areas to established techniques of practical and industrial relevance within the scope of the Circuits and Systems Society.

Tutorial Proposals Submission Guidelines

Please submit tutorial proposals by email to the Tutorial Co-Chairs Farshad Moradi (moradi@eng.au.dk), Pedram Mohseni (pxm89@case.edu), and Jeff Weldon (jaweldon@hawaii.edu) by Dec. 20, 2022. The proposal should include a two-page (maximum) description of the tutorial (see below for details) and one-page professional biographies of the tutorial speakers that highlight their qualifications and track records in the proposed subject area of the tutorial. The proposal needs to contain the following information:

1. Format of the Tutorial
   Full-day or Half-day.

2. Title and Abstract
   Propose a title and an abstract that allows conference attendees to quickly and accurately understand the subject matter of the tutorial.

3. Motivation and Focus
   Explain why your topic is important to the ISCAS community and what you hope the participants will learn by attending your tutorial. In this section, please provide a syllabus outlining the contents of the tutorial and the learning outcomes. This syllabus should be realistic, given the time available.

4. List of Speakers
   This is in addition to the tutorial organizers. Some tutorials may feature different speakers that should be advertised, but it is not necessary, nor a requirement. For instance, a full-day tutorial may have, at most, four different speakers, while a half-day tutorial could have up to two speakers. If you plan to have multiple speakers, please provide their names and affiliations and clearly describe the parts of the tutorial that they will cover.

5. Basic Structure of the Tutorial
   In this section, please describe your use of the time available. (e.g., morning lectures: fundamentals and case studies; afternoon panel discussion or brainstorming sessions, etc.).

Authors of each of the two most successful tutorials will be invited to write a corresponding overview paper in the Circuits and Systems Magazine or in the IEEE Transactions on Circuits and System – Part I: Regular Papers, or IEEE Transactions on Circuits and Systems II: Express Briefs (TCAS–II).

Please visit: iscas2023.org
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