



## Meeting Minutes

Nanoelectronics and Gigascale Systems TC Meeting at ISCAS 2010

Date: May 31, Monday, 2010, 12:50 ~ 14:00

Place: Coyotte (Sequoia Lodge)

Chairman: Prof. Wei Wang; Secretary: Prof. Shyh-Jye Jou

### Attendance:

Wei Wang, University at Albany, [wwang@uamail.albany.edu](mailto:wwang@uamail.albany.edu)

Shyh-Jye Jou, National Chiao Tung University, [jerryjou@mail.nctu.edu.tw](mailto:jerryjou@mail.nctu.edu.tw)

Wai-Chi Fang, National Chiao Tung University, [dr.wfang@gmail.com](mailto:dr.wfang@gmail.com)

Chua-Chin Wang, National Sun Yat-Sen University

Malgorzata Chrzanowska-Jeske, Portland State Univ., [jeske@ece.polx.edu](mailto:jeske@ece.polx.edu)

Chen-Hao Chang, National Chung-Hsin University, [chchang@dragon.nchu.edu.tw](mailto:chchang@dragon.nchu.edu.tw)

Chih-Wei Liu, National Chiao Tung University, [cwliu@twins.ee.nctu.edu.tw](mailto:cwliu@twins.ee.nctu.edu.tw)

Ching-Te Chiu, National Tsing-Hua University, [ctchiu@cs.nthu.edu.tw](mailto:ctchiu@cs.nthu.edu.tw)

Garrett Rose, New York University, [grose@poly.edu](mailto:grose@poly.edu)

Ming Liu, Institute of Microelectronics, Chinese Academy of Sciences, [liuming@ime.ac.cn](mailto:liuming@ime.ac.cn)

Angel Rodriguez-Vazquez, IMSE-CNM/CSIC, [angel@imse-cnm.csic.es](mailto:angel@imse-cnm.csic.es)

Hao Yu, NanYang University of Singapore, [haoyu@ntu.edu.sg](mailto:haoyu@ntu.edu.sg)

Nathan Cady, State University of New York at Albany, [ncady@uamail.albany.edu](mailto:ncady@uamail.albany.edu)

Haigang Yang, Chinese Academy of Science, [yanghg@mail.ie.ac.cn](mailto:yanghg@mail.ie.ac.cn)

Hyunchol Shin, Kwangwoon Univ., [hshin@kw.ac.kr](mailto:hshin@kw.ac.kr)

Yuan-Hao Huang, National Tsing-Hua University, [yhhuang@ee.nthu.edu.tw](mailto:yhhuang@ee.nthu.edu.tw)

Zhirun Hu, University of Manchester, [z.hu@manchester.ac.uk](mailto:z.hu@manchester.ac.uk)

## Agenda

1. Opening of meeting
2. Introduction and welcome
3. Approval of the meeting agenda
4. Approval of past meeting minutes
5. Introduction and approval of new members (Vote new members)
6. Report on accomplished technical activities (May 2009 to May 2010)
7. Planning of upcoming events and near future activities
8. Award recognition and endorsement
9. New business
10. Adjournment

New TC member Candidates: (CV is available in the meeting.)

Hao Yu, NanYang University of Singapore, [haoyu@ntu.edu.sg](mailto:haoyu@ntu.edu.sg)

Nathan Cady, State University of New York, [ncady@uamail.albany.edu](mailto:ncady@uamail.albany.edu)

Haigang Yang, Chinese Academy of Science, [yanghg@mail.ie.ac.cn](mailto:yanghg@mail.ie.ac.cn)

Hyunchol Shin, Kwangwoon Univ., [hshin@kw.ac.kr](mailto:hshin@kw.ac.kr)

Yuan-Hao Huang, National Tsing-Hua University, [yhhuang@ee.nthu.edu.tw](mailto:yhhuang@ee.nthu.edu.tw)

Zhirun Hu, University of Manchester, [z.hu@manchester.ac.uk](mailto:z.hu@manchester.ac.uk)

1. Opening of meeting  
At 13:15, Chair Wei Wang called to order of the meeting.
2. Introduction and welcome  
Chairman made welcome speech and attendees made brief introduction of affiliation and research interests.
3. Approval of the meeting agenda  
Approved.
4. Approval of past meeting minutes  
Chua-Chin Wang made motion to pass the past meeting minutes and Wai-Chi Fang made second motion. The attendance make a vote to pass the meeting minutes.
5. Introduction and approval of new members (Vote new members)  
There are six applications for new member:  
Hao Yu, NanYang University of Singapore, haoyu@ntu.edu.sg  
Nathan Cady, State University of New York, [ncady@uamail.albany.edu](mailto:ncady@uamail.albany.edu)  
Haigang Yang, Chinese Academy of Science, yanghg@mail.ie.ac.cn  
Hyunchol Shin, Kwangwoon Univ., hshin@kw.ac.kr  
Yuan-Hao Huang, National Tsing-Hua University, yhhuang@ee.nthu.edu.tw  
Zhirun Hu, University of Manchester, z.hu@ Manchester.ac.uk  
Each new member made brief introduction on his specialty and statement. Chairman asked new members to temporarily leave the meeting room and members discussed each case. All the new members are unanimous approved.
6. Report on accomplished technical activities (May 2009 to May 2010)  
Please see the 2010 annual report in Appendix A made by Chairman
7. Planning of upcoming events and near future activities
  - (1) Submit at least two special sessions proposal for 2011 ISCAS
  - (2) Solicit around 100 good papers to submit to 2011 ISCAS
  - (3) Recruit new TC members
  - (4) Organize tutorial and nano-giga related workshop
8. Award recognition and endorsement  
Chairman encouraged member to make applications for all kinds of service, award recognition at IEEE CAS, especially, Distinguished Lecture Program (DLP) and board of governor.
9. New business  
Encourage more activities on Nanoelectronics and Gigascale Systems activities to broaden the scope of IEEE CAS society.
10. Adjournment

## Appendix A 2010 Annual Report

### 1 IEEE Services

#### 1.1 Invited talks

- Keynote Speech: "Design Challenges for High-Performance Three-Dimensional Circuits," Keynote Presentation, D43D: System Design for 3D Silicon Integration Workshop, Grenoble, France, June 18, 2009, E. G. Friedman.
- Invited Talk: "Advances in a CMOS Image Sensor based miniature Fluorescence Detection System for Bio-sensing Applications", invited for Delft University Workshop on Imaging, December 2009, Orly Yadid-Pecht.
- Invited Talk: "Wide dynamic range sensors – progress in extension on both high and low level sides", invited for Hong Kong University Workshop, November 2009, Orly Yadid-Pecht.
- Invited talk: "Creatinine and Neopterin Biosensors for Diagnosis of Renal Function and Cellular Immunity," 2009 IEEE Circuits and Systems International Conference on Testing and Diagnosis (ICTD 2009). (Chengdu, China), Bin-Da Liu.
- Invited Talk: "The Rochester Cube and Other 3-D Circuits for Clock and Power Delivery," 3-D Architectures for Semiconductor Integration and Packaging, December 10, 2009, E. G. Friedman.
- Invited Talk: DLP lecture, IEEE CAS Tainan Chapter, "Circuits, Systems and Verification Platform for 10 Gbps On-Chip Transmission Link", Tainan, Taiwan, Oct. 28, 2009, Shyh-Jye Jou.
- Invited Talk: "Reconfigurable logic based on graphene PN junctions," ECS Symposium: Graphene, Ge/III-v, and Emerging Materials for Post-CMOS Applications, Vancouver, Canada, April 2010, S. Tanachutiwat and Wei Wang.
- Invited Talk: "Developing ReRAM for CMOS integration," IEDM/Sematech Emerging Technology Workshop, Baltimore, MD, USA, Dec. 2009, Wei Wang.
- Invited Talk: "Nano-ReRAM for novel FPGA architectures," SRC-Astar Emerging Memory Workshop, Singapore, Oct. 2009, Wei Wang.
- Invited Talk: "21<sup>st</sup> Century Competitiveness of Youth in Taiwan, Coopetition Between Talents and Technologies," Winter Teachers Camp, National Chiao Tung University, Feb. 2010, Bing Sheu.
- Invited Talk: "21<sup>st</sup> Century Youth to Master Innovations," Computer Science Department, National Chiao Tung University, May 12, 2010, Bing Sheu.
- Invited Talk: "21<sup>st</sup> Century Youth to Foresee the Future, Embrace the Future, and Win the Future," Graduate Institute of Communication Engineering, National Taiwan University, May 19, 2010, Bing Sheu.
- Invited Talk: Panelist on Industry Panel, ISCAS, Paris, France, June 2010, Bing Sheu.

- Invited Talk: “Multi-Layer Cooperative Error Control for Reliable Energy-Efficient NoCs”, UNIVERSITY OF WISCONSIN, Madison WI, Oct. 2009, Paul K. Ampadu.
- Invited Talk: “Reliable Hybrid Electronic/Photonic Network-on-Chip”, UNIVERSITY AT ALBANY-SUNY Semiconductor Research Corporation NY Center for Advanced Interconnect Science & Technology (NYCAIST) Fall Review & Presentation, Albany NY, Oct. 2009, Paul K. Ampadu.
- Invited Talk: “Multi-Layer Cooperative Error Control for Heterogeneous Nanoscale NOCs”, COLUMBIA UNIVERSITY Computer Science Seminar Series, New York NY, Sep. 2009, Paul K. Ampadu.
- Invited Talk: “Error Control for Hybrid CMOS/Nanoelectronics Networks-on-Chip”, UNIVERSITY OF ROCHESTER ECE Seminar Series, Rochester NY, Sep. 2009, Paul K. Ampadu.
- Invited Talk: “Error Control for On-Chip Interconnect Networks”, Emerging Information & Technology Conference, MASSACHUSETTS INSTITUTE OF TECHNOLOGY, Cambridge MA, Aug. 2009, Paul K. Ampadu.
- Invited Talk: “Towards THz-pJ Reliable Computation & Communication”, NATIONAL TAIWAN UNIVERSITY, Taipei, Taiwan, May 2009, Paul K. Ampadu.
- Invited Talk: “Ballistic Deflection Transistors and the Emerging Nanoscale Era”, INTERNATIONAL SYMPOSIUM ON CIRCUITS & SYSTEMS (ISCAS’09) Special Session on Emerging Nanoelectronics, Taipei, Taiwan, May 2009, Paul K. Ampadu.
- Invited Talk, “Casta DIVA - A Generic Introspective Architecture for Design for Reliability”, University College Cork, Ireland, January 2010, Sorin Cotofana.
- Invited Talk, “Nano-Device Based Computation”, UPC Barcelona, Spain, July 2009, Sorin Cotofana.

## 1.2 Organizers: Conferences, Workshops, Panels, Special Sessions, Tutorials

- Special Session organizer, “Green Technologies for Reliable Circuits and Advanced Systems”, International Conference on Green Circuits and Systems (ICGCS), 2010, Lan-Da Van.
- Special Session organizers, “Memristors and memristive systems--from devices to applications,” ISCAS 2010, May 2010, G. Rose and Wei Wang.

- Special Session co-organizer, “Emerging FPGA technologies,” ISCAS 2010, May 2010, Wei Wang.
- Special Session Co-organizer, “Design Methodologies for Low Power Sensory and Memory Arrays”, IEEE Sensors Conference, October 2009, Christchurch, New Zealand, Orly Yadid-Pecht.
- 2010 Inter. Symp. on Next-generation Electronics (ISNE), General Co-Chair, Chua-Chin Wang.
- 2010 IEEE ISCAS, Session Chair: Chih-Wei Liu.
- 2010 IEEE VLSI-DAT, Session Chair: Chih-Wei Liu.
- 2010 IEEE VLSI-DAT, TPC Co-Chair, reviewer: Chih-Wei Liu.
- Co-Chair of Technical Program Committee, the 2009 International Symposium on Circuits and systems (ISCAS 2009). (May 24-27, 2009, Taipei, Taiwan), Bin-Da Liu.
- Co-Chair of Technical Program Committee, International Conference on Computer, Communications, Circuits and Systems (ICCCAS 2009). (July 23-25, 2009, San Jose, USA), Bin-Da Liu.
- Co-Chair of Technical Program Committee, the 2010 International Conference on Computer, Communications, Circuits and Systems (ICCCAS 2010). (July 28-30, 2010, Chengdu, China), Bin-Da Liu.
- Technical Program Committee and Tutorial Chair of the IEEE International Symp. on VLSI Design, Automation and Test, Hsinchu, 2010, Shyh-Jye Jou.
- ISCAS 2010, International Liaison; Session Chairs/Co-Chairs; RCM: Ming-Dou Ker.
- ICICDT 2010 (International Conference on IC Design & Technology, Grenoble, France; June 2 – June 4, 2010), Sub-Committee Chairs/Co-Chairs; Session Chairs/Co-Chairs: Ming-Dou Ker.
- 2010 IEEE ISCAS, Nano-Giga TC Track Co-Chair: Chua-Chin Wang, Wei Wang.
- IEEE Workshop on Signal Processing Systems workshop proposal co-organizer (SiPS) (2011), Workshop Title: Signal Processing for Green Lifestyle, Ching-Te Chiu.
- 2010 IEEE ICASSP, Session Chair: Ching-Te Chiu.
- 2010 IEEE ICASSP Signal Processing Education Technical Committee DISPS Liaison: Ching-Te Chiu.
- 2010 IEEE ISCAS, Session Chair: Ching-Te Chiu.
- 2009 IEEE ISCAS, Session Chair: Ching-Te Chiu.
- IEEE ISCAS 2010, Session Chair, RCM, Reviewer, Shyh-Jye Jou.
- ISCAS, NOCS, Session Chair and RCM, Paul K. Ampadu.
- ISCAS'09, Session Chair (2 sessions), Review Committee Member, Malgorzata Chrzanowska-Jeske.

- 2009 CMOS Emerging Technology Workshop, Technical Committee, Session Chair (1), Malgorzata Chrzanowska-Jeske.
- ISCAS 2010, Tutorials co-chair, Session Chair (2), Malgorzata Chrzanowska-Jeske.
- 2010 CMOS Emerging Technology Workshop, Technical Committee, Session Chair (1), Malgorzata Chrzanowska-Jeske.
- 2009 DLP Chair, Malgorzata Chrzanowska-Jeske.
- 2010 DLP Chair, Malgorzata Chrzanowska-Jeske.
- Design Contest Chair and TPC of the IEEE Asian Solid-State Circuit Conference, Nov. 2009, Shyh-Jye Jou.
- 2009 VLSI/CAD Symp., Steering Committee, Chua-Chin Wang.
- 2009 IEEE ISCAS, Nano-Giga TC Track Chair, RCM members, Chua-Chin Wang.
- 2009 Inter. Symp. on VLSI Design, Automation and Test (VLSI-DAT), TPC member, Session Chair, Chua-Chin Wang.
- 2010 International Conference on IC Design and Technology (ICICDT), Track Chair & TPC member, Chua-Chin Wang.
- Programme committee co-chair, International Conference on Nano-Networks, Nano-NET 2009, Sorin Cotofana.
- Programme committee chair, The International Conference on Quantum, Nano, and Micro Technologies 2009, Sorin Cotofana.
- Program Leader on Talents Nurturing, National SoC Program, Taiwan National Science Council, Bing Sheu.
- Head of “TSMC University Shuttle Program”, Bing Sheu.
- 2009 IEEE ASSCC, Local Organizing Committee member, Chua-Chin Wang.
- 2010 IEEE ISCAS, RCM member, TPC member, reviewer: Chih-Wei Liu.
- 2010 VLSI Design/CAD Symp., TPC member: Chih-Wei Liu.
- TC Member of VLSI Design/CAD Symposium: Lan-Da Van.
- TC Member of International Conference on Green Circuits and Systems (ICGCS) (2010): Lan-Da Van.
- Program committee member, IEEE/ACM International Symposium on Nanoscale Architectures NANOARCH 2009, 2010, Sorin Cotofana.
- Program committee member, Design, Automation and Test in Europe DATE 2008 and 2009, 2010, Sorin Cotofana.
- Program committee member, High Performance Computing & Simulation (HPC&S) Conference, 2010, Sorin Cotofana.
- Program committee member, IEEE/IFIP International Conference on VLSI and System-on-Chip (VLSI-SoC), 2010, Sorin Cotofana.
- Program committee member, 1st IEEE Workshop on design, control and software implementation of distributed MEMS, 2010, Sorin Cotofana.

- Member of the IEEE CAS Neural Networks, Biocas and Sensors Technical Committees (1996 – present), Orly Yadid-Pecht.
- Member of the SPIE Solid State Sensor Arrays international conference program committee (1997-present), Orly Yadid-Pecht.
- Member of the Technical Committee for the IEEE BioCAS conference (2004-present), Orly Yadid-Pecht.
- Member of the Steering Committee for the IEEE ICECS (2003-present), Orly Yadid-Pecht.
- Member of the IEEE CAS Women in Engineering committee, Orly Yadid-Pecht.
- ICECS'09, Steering Committee, Technical Committee, Malgorzata Chrzanowska-Jeske.
- ISCAS 2009, Steering Committee, Malgorzata Chrzanowska-Jeske
- ISLPED 2009, Technical Committee, Malgorzata Chrzanowska-Jeske
- ISLPED 2010, Technical Committee, Malgorzata Chrzanowska-Jeske
- ICECS 2010, Steering Committee, Program Committee, Tutorials c-Chair, Malgorzata Chrzanowska-Jeske
- ISCAS 2010 – WiCAS presentation and WiCAS/GOLD dinner organizer, Malgorzata Chrzanowska-Jeske
- 2009 BoG member, Malgorzata Chrzanowska-Jeske.
- 2009 CASS Conference Activities Committee, Malgorzata Chrzanowska-Jeske.
- 2009 CASS, Long-Term Strategy Committee, Malgorzata Chrzanowska-Jeske.
- 2010 BoG member, Malgorzata Chrzanowska-Jeske.
- 2010 WiCAS Chair, Malgorzata Chrzanowska-Jeske.
- 2010 CASS Regional Activities Committee, Malgorzata Chrzanowska-Jeske.
- 2010 CASS, Long-Term Strategy Committee, Malgorzata Chrzanowska-Jeske.
- 2010 CAS Representative to IEEE Women in Engineering Committee, Malgorzata Chrzanowska-Jeske.
- IEEE ISCAS 2010, RCM, Reviewer, Hsiang-Cheh Huang.
- 2009 Int'l Conf. on Intelligent Information Hiding and Multimedia Signal Processing, PC member: Hsiang-Cheh Huang.
- 2009 Int'l Conf. on Hybrid Intelligent Systems, PC member: Hsiang-Cheh Huang.
- 2009 Int'l Conf. on Information Assurance and Security, PC member: Hsiang-Cheh Huang.
- 2010 Int'l Conf. on Intelligent Information Hiding and Multimedia Signal Processing, Finance Chair: Hsiang-Cheh Huang.
- International Advisor, the First Asia Pacific Conference on Postgraduate Research in Microelectronics & Electronics (PrimeAsia 2009). (Nov. 19-21, 2009, Shanghai, China), Bin-Da Liu.



- International Advisor, the Second Asia Pacific Conference on Postgraduate Research in Microelectronics & Electronics (PrimeAsia 2010). (Sep. 22-24, 2010, Shanghai, China), Bin-Da Liu.
- 2010 IEEE NanoArch, TPC member: Wei Wang.
- IASTED, DFTS, NOCS, ICAST, Program Committee Member, Paul K. Ampadu.
- IEEE Circuits & Systems Society, VSA TC, NanoGiga TC, CASCOM TC, Paul K. Ampadu.
- IEEE CAS, SSCS, SPS, and Computer Societies, Member, Paul K. Ampadu.
- 2009 IEEE ISCAS, reviewer, RCM member: Wei Wang, Ming-Dou (Morris) Ker, Malgorzata Chrzanowska-Jeske.
- 2010 IEEE ISCAS, reviewer, RCM member: Chua-Chin Wang, Wei Wang.
- Member of International Program Committee, the 2010 IEEE International Conference on Systems, Man, and Cybernetics (SMC 2010) (October 10-13, 2010, Istanbul, Turkey), Bin-Da Liu.
- TC Member of VLSI Design/CAD Symposium: Ching-Te Chiu.
- TC Member of IEEE Signal Processing: Design and Implementation for Signal Processing Systems (DISPS) (2010): Ching-Te Chiu.
- 2010 ASP-DAC University LSI Design Contest Committee, reviewer: Chih-Wei Liu.
- 2010 IEEE SiPS, reviewer: Chih-Wei Liu.
- Reviewer of IEEE TCAS-I: Lan-Da Van.
- Reviewer of IEEE TCAS-II: Lan-Da Van.
- Reviewer of IEEE TVLSI: Lan-Da Van.
- Reviewer of Elsevier--Computer and Electrical Engineering: Lan-Da Van.
- Review Committee Member for Multimedia Systems and Applications Track, the 2010 IEEE International Symposium on Circuits and Systems (ISCAS 2010). (May 30-June 2, 2010, Paris, France), Bin-Da Liu.
- Review Committee Member of 2010 International Conference on Green Circuits and Systems (ICGCS 2010). (June 21-23, 2010, Shanghai, China), Bin-Da Liu.
- Reviewer of IEEE Transactions on VLSI, Journal of VLSI Signal Processing, VLSI Journal, Journal of Circuits, Systems & Computers, Microelectronics Journal, IET Computers & Digital Techniques, IEEE Transactions on Circuits & Systems I and II, Int. Journal of Circuit Theory & Applications, Communication Letters, Paul K. Ampadu.

### 1.3 Editorial Services

- 2007 – 2009, IEEE Trans. on BioMedical Circuits and Systems Associate Editor, Orly Yadid-Pecht.
- 2006-present, IEEE Sensors Council – Circuits and Systems Society Representative, Board Member (2006-present), Orly Yadid-Pecht.

- Associate Editor, IEEE Transactions on Circuits and Systems-II, Bin-Da Liu.
- Associate Editor, IEEE Transactions on Fuzzy Systems, Bin-Da Liu.
- Associate Editor, IEEE Transactions on Biomedical Circuits and Systems, Bin-Da Liu.
- Associate Editor, Journal of Electrical and Computer Engineering, Bin-Da Liu.
- US Regional Editor, Journal of Circuits, Systems and Computers, E. G. Friedman.
- Analog Integrated Circuits and Signal Processing, E. G. Friedman.
- Journal of VLSI Signal Processing Systems for Signal, Image, and Video Technology / Signal Processing Systems, E. G. Friedman.
- Microelectronics Journal, E. G. Friedman.
- Journal of Low Power Electronics, E. G. Friedman.
- Inter. J. of VLSI Design, Associate Editor, Chua-Chin Wang.
- IEICE Trans. on Electronics, Associate Editor, Chua-Chin Wang.
- J. of Signal Processing Systems, Associate Editor & Guest Editor, Chua-Chin Wang.
- IEEE Trans. on Circuits & Systems-I: Regular Papers, Associate Editor, Chua-Chin Wang.
- IEEE Trans. on Circuits & Systems-II: Brief Papers, Associate Editor, Chua-Chin Wang.
- Journal of Circuits, Systems, and Computers (JCSC), Associate Editor, Paul K. Ampadu.
- Associate Editor, IEEE Transactions on Nanotechnology, Sorin Cotofana.
- Associate Editor, IEEE Transactions on Circuits and Systems I, Sorin Cotofana.
- Associate Editor, International Journal of Computer Aided Engineering and Technology (IJCAET), Sorin Cotofana.
- Editor on Editorial Board, Proceedings of the IEEE, 2008 – 2010, Bing Sheu.
- IEEE Transactions on Circuits and Systems I, reviewer: Chih-Wei Liu.
- IEEE T. on CAS-II, reviewer: Chih-Wei Liu.
- IEEE Transactions on VLSI Systems, reviewer: Chih-Wei Liu.
- IEEE T. on CSVT, reviewer: Chih-Wei Liu.
- Reviewer of IEEE Communication Letters: Hsiang-Cheh Huang.
- Reviewer of IEEE Trans. on CSVT: Hsiang-Cheh Huang.
- Reviewer of IEEE Trans. on Communications: Hsiang-Cheh Huang.
- Reviewer of IEEE Trans. on Image Processing: Hsiang-Cheh Huang.
- Reviewer of IEEE Trans. on Information Forensics and Security: Hsiang-Cheh Huang.
- Reviewer of IEEE Trans. on SMC, part C: Hsiang-Cheh Huang.
- Reviewer of Elsevier--Information Sciences: Hsiang-Cheh Huang.
- Reviewer of Elsevier--Digital Signal Processing: Hsiang-Cheh Huang.

- Reviewer of Elsevier--Soft Computing: Hsiang-Cheh Huang.
- IEEE Transactions on VLSI Systems, reviewer: Ching-Te Chiu.
- IEEE Transactions on Circuits and Systems for Video Technologies, reviewer: Ching-Te Chiu.
- IEEE Transactions on Image Processing, reviewer: Ching-Te Chiu.
- J. of Signal Processing Systems, reviewer: Ching-Te Chiu.
- Review Committee Member, The IEEE International Symposium on Circuits and Systems (ISCAS) 2009, Sorin Cotofana.

1.4 Other Professional Activities (Here: anything you deem relevant and worth mentioning.)

- Chair of steering committee, IEEE Transactions on Very Large Scale Integration (VLSI) Systems, E. G. Friedman.
- Member of steering committee, IEEE International Conference on Electronics, Circuits, and Systems, E. G. Friedman.
- Member of steering committee, IEEE International Symposium on Networks on Chip (NoC), E. G. Friedman.
- Membership Chair, IEEE Tainan Section, Chua-Chin Wang.
- Chairman, Dept. of EE, National Sun Yat-Sen University, Chua-Chin Wang.
- Director of Program 2, National SoC Program, Taiwan, Chua-Chin Wang.

## 2 Awards and Honors

- 2009 NSF CAREER Award, Paul K. Ampadu.
- 2009-2010 IBM Faculty Award, Wei Wang.
- 2009 iCORE Professorship in “Integrated Sensors and Intelligent Systems”, Orly Yadid-Pecht.
- 2009 Charles Drew Professor’s Choice Award, Paul K. Ampadu.
- 2009 Black Engineer of the Year Special Recognition Award, Paul K. Ampadu.
- 2009 IEEE Asian Solid State Circuits Conference, Student Design Contest Outstanding Design Award, Shyh-Jye Jou.
- 2009 Chinese Academy of Science Top 10 Women Scholar Award, Ming Liu.
- 2009 Chinese Vacuum Society Outstanding Paper Award, Qi Liu and Ming Liu.
- 2009 Inventor Award, National Sun Yat-Sen University, Chua-Chin Wang.
- 2009 Academic Keys Who’s Who in Engineering Academia, Paul K. Ampadu.