



















































IEEE CIRCUITS AND SYSTEMS SOCIETY

TECHNICAL COMMITTEE ON CELLULAR NANOSCALE NETWORKS AND ARRAY COMPUTING (CNNAC)

ANNUAL REPORT 2017

MEMBERS

 Akio Ushida	 Eduardo Gomez	 Leon O. Chua	 Arena, Paolo
 Akos Zarandy	 Fernando Corinto	 Luigi Fortuna	 Pier Paolo Civalleri
 Dr. Alon Ascoli	 Georgios Ch. Sirakoulis	 Maciej Ogorzalek	 Piotr Dudek
 Angel Rodriguez-Vazquez	 Gert Cauwenberghs	 Mamoru Tanaka	 Ricardo Carmona Galán
 Angela Slavova	 Horvath Andras	 Marco Gilli	 Ronald Tetzlaff
 Ari Paasio	 Hyongsuk Kim	 mattia.frasca@diee	 Sabri Arik
 Arpad Csurgay	 Johan Suykens	 Mauro Di Marco	 Szolgay Peter
 Bertram Shi	 Joos Vandewalle	 Michael Niemier	 Valeri Mladenov
 Chai Wah Wu	 Jordi Albo-Canals	 Mika Laiho	 Vedat Tavşanoğlu
 Chin-Teng Lin	 Jorge Fernández Berni	 Müştak Yalçın	 Victor M.Brea Sánchez
 Chung-Yu Wu	 Josef Nossek	 Nagy Zoltan	 Wolfgang Porod
 Csaba Rekeczky	 Kristof Karacs	 Nerhun Yildiz	 Xavier Vilasis Cardona
 David Lopez Vilarino	 Lambert Spaanenburg		

Chair : Mustak E. Yalcin

Chair-elect :Fernando Corinto

RENEWAL OF OFFICERS

Chair :

Dr. Fernando Corinto(May 2017 – May 2019)

Associate Professor
Politecnico di Torino
Department of Electronics and Telecommunications
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Personal Web Page : <http://personal.delen.polito.it/fernando.corinto/Curriculum.html>

Google Scholar: <https://scholar.google.it/citations?user=9qwNrGUAAAAJ&hl=en>

Research Gate : https://www.researchgate.net/profile/Fernando_Corinto

Secretary :

Dr. Alon Ascoli

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Tel. +49 351 463-33826
Fax +49 351 463-37042
e-mail: alon.ascoli@tu-dresden.de

Research Gate: https://www.researchgate.net/profile/Alon_Ascoli

Past Chair :

Dr. Mustak E. Yalcin

Professor
Istanbul Technical University
Faculty of Electrical and Electronic Engineering
Department of Electronics and Communication Engineering
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Tel: 00-90-212-285 36 12
Fax:00-90-212-285 35 65
E-mail: mustak.yalcin@itu.edu.tr

Personal Webpage: <http://www2.itu.edu.tr/~yalcinmust>

Google Scholar: <https://scholar.google.be/citations?hl=en&user=tjv7hI4AAAAJ>

Research Gate : https://www.researchgate.net/profile/Mustak_Yalcin

CONFERENCES/WORKSHOPS WHERE THE MEMBERS OF THE TC WERE ACTIVELY INVOLVED

- The IEEE International Symposium on Circuits and Systems (ISCAS) in Montreal, Canada, from May 22 to 25, 2016.
 - SPECIAL SESSION: “Memristors I”, Chair: Ronald Tetzlaff(CNNAC TC Member) and Alon Ascoli(CNNAC TC Member).
 - SPECIAL SESSION: “Memristors II”, Chair: Ronald Tetzlaff(CNNAC TC Member) and Alon Ascoli(CNNAC TC Member).
 - SPECIAL SESSION: “Brain Interfaces”, Chair: Roland Thewes and Ronald Tetzlaff(CNNAC TC Member).
 - SPECIAL SESSION: “Kirchhoff Paradigm, Wave Digital Filters, and More: A Tribute to Alfred Fettweis I”, Chair: J. A. Nossek (CNNAC TC Member) and H. C. Reddy
 - SPECIAL SESSION: “Kirchhoff Paradigm, Wave Digital Filters, and More: A Tribute to Alfred Fettweis II”, Chair: J. A. Nossek (CNNAC TC Member) and H. C. Reddy
- The 15th International Workshop on Cellular Nanoscale Networks and their Applications in Dresden, Germany from August 23 to 25 (<https://www.iee.et.tu-dresden.de/cnna2016/>)



- CNNAC TC Members in HONORY CHAIR:
 - Leon O. Chua
- CNNAC TC Members in the General Chairs:
 - Ronald Tetzlaff
 - Angela Slavova
- CNNAC TC Members in the Scientific Committee:
 - Paolo Arena
 - Leon O. Chua
 - Fernando Corinto
 - György Csaba
 - Piotr Dudek
 - Peter Földesy
 - Luigi Fortuna
 - Marco Gilli
 - András Horváth
 - Michael Niemier
 - Josef A. Nossek
 - Maciej Ogorzalek
 - Ari Paasio
 - Giovanni E. Paziienza
 - Wolfgang Porod
 - Csaba Rekeczky
 - Ángel Rodríguez-Vázquez

- Bertram Shi
 - Péter Szolgay
 - Mamoru Tanaka
 - Vedat Tavsanoğlu
 - Ronald Tetzlaff
 - Joos Vandewalle
 - Xavier Vilasís-Cardona
 - Chai Wah Wu
 - Mustak E. Yalcin
 - Ákos Zarándy
 - Invited speaker: “Nanomagnetic Computing”, by Wolfgang Porod (CNNAC TC Member)
 - Invited speaker: “Collaborating Smart Lighting Networks”, by Csaba Rekeczky, (CNNAC TC Member)
 - Invited speaker: “On the Design of Neural Recording Interfaces”, by Ángel Rodríguez-Vázquez (CNNAC TC Member)
- **The 5th Memristor and Memristive Symposium in Dresden, Germany on August 22, 2016**
(https://www.iee.et.tu-dresden.de/cnna2016/wp-content/uploads/2016/08/program_5th-Memristor-and-Memristive-Symposium.pdf)
 - CNNAC TC Members in the Organization Committee:
 - Ronald Tetzlaff
 - Alon Ascoli
 - Invited speaker from our TC:
 - Fernando Corinto, Politecnico di Torino, Italy: “Memristor Circuits: Flux–Charge Analysis Method“
 - Ronald Tetzlaff, Technische Universität Dresden, Germany: “The role of initial conditions in memristors“
 - Leon Chua, University of California, Berkeley, USA: “Non-Volatile ↔ POP is Flat“
 - **The VIII IEEE Latin American Symposium on Circuits and Systems (LASCAS) 2017**
 - Invited talk: "Why FinFETs, Why 3D ICs - are we ready for technology revolution? ", by Maciej Ogorzalek, (CNNAC TC Member) and Malgorzata Chrzanowska-Jeske.
 - **The International Conference on Modern Circuits and Systems Technologies (MOCAST), 12 - 14 May 2016 / Thessaloniki, Greece**
 - CNNAC TC Member in Special Sessions Co-chair:
 - Georgios Sirakoulis
 - CNNAC TC Members in Program Committee:
 - Alon Ascoli
 - Georgios Sirakoulis
 - Mustak Yalcin
 - **IEEE International Solid-State Circuits Conference (ISSCC 2017), San Francisco , USA Feb 5- 9 2016**
 - CNNAC TC Member in the Program Committee: Gert Cauwenberghs
 - **VLSI Summer School, IIT Kharagpur, May 27, 2016**
 - Invited talk, “Reverse Engineering the Cognitive Brain in Silicon,” Gert Cauwenberghs (CNNAC TC Member)
 - **The International Conference on Distributed Smart Cameras (ICDSC 2016) in Paris, France, September 12-15, 2016.**
 - CNNAC TC Member in Technical Program Committee:
 - Víctor Brea,
 - Ricardo Carmona-Galán

- [Ángel Rodríguez-Vázquez](#)
 - [Jorge Fernández-Berni, \(CNNAC TC Member\)](#)
 - [V́ctor Brea, \(CNNAC TC Member\)](#)
- [Workshop on Architecture of Smart Camera \(WASC 2016\), Dijon, France, July 4--5, 2016.](#)
 - CNNAC TC Member in Advisory board :[Ricardo Carmona-Galán](#)
- [ICASSP, 2016, China 20-25 March 2016](#)
 - Plenary Speaker: [Johan Suykens \(CNNAC TC Member\)](#), "Learning with Primal and Dual Model Representations: A Unifying Picture"
- [IEEE Biomedical Circuits and Systems Conference \(BioCAS 2016\), Shanghai, Oct. 17-19, 2016.](#)
 - CNNAC TC Member in International Liaison Committee: [Gert Cauwenberghs](#)
- [IEEE Engineering in Medicine and Biology Conference \(EMBC 2017\);](#)
 - CNNAC TC Member in the Organization Committee: [Gert Cauwenberghs](#)
- [Global Healthcare Forum, Cambridge MA, March 25 -26, 2017.](#)
 - Invited talk, "Silicon Integrated High-Density Neural Interfaces," [Gert Cauwenberghs \(CNNAC TC Member\)](#)
- [5th International Conference from Nanoparticles and Nanomaterials to Nanodevices and Nanosystems \(5th IC4N 2016\).](#)
 - Invited Speaker: [Georgios Sirakoulis](#)
- [IRDS NanoCrossbar Workshop, Sunnyvale CA, July 15, 2016](#)
 - Invited talk, "Neuromorphic Silicon Learning Machines," [Gert Cauwenberghs \(CNNAC TC Member\)](#)
- [International IEEE EMBS Workshop on Advanced NeuroTechnologies for BRAIN Initiatives \(ANTBI\), Nov. 10-11, San Diego, 2016.](#)
 - Invited talk : "Silicon Integrated High-Density Neural Interfaces,"[Gert Cauwenberghs \(CNNAC TC Member\)](#)
- [IEEE Neural Engineering Conference \(NER 2017\), Shanghai, China, May 25-28 2017.](#)
 - CNNAC TC Member in Program Committee:[Gert Cauwenberghs](#)
- [IEEE Symposium Series on Computational Intelligence \(IEEE SSCI 2016\)](#)
 - CNNAC TC Member in Special Session Co-Chair:[Georgios Sirakoulis](#)
- [Workshop on Complexity in Engineering, Catania, Italy, July 4-6, 2016](#)
 - CNNAC TC Member in General Chair: [Luigi Fortuna](#)
 - CNNAC TC Member in Steering Committee: [Mattia Frasca](#)
 - Plenary talk : "Applications of Cellular Automata with Random Memory: Random NumberGenerator and Physical Unclonable Function" by [Mustak E. Yalcin \(CNNAC TC Member\)](#)
- [IGERT Neuroengineering Training Program, University of Minnesota, Minneapolis, May 16, 2017.](#)
 - Keynote Speaker: "Advances in Multiscale Integrated Neural Interfaces," [Gert Cauwenberghs \(CNNAC TC Member\)](#)
- [12th International Conference on Cellular Automata for Research and Industry \(ACRI 2016\).](#)
 - CNNAC TC Member in Steering Committee:[Georgios Sirakoulis](#)
- [NSCI Seminar, Boulder CO, April 11, 2017](#)
 - Invited talk . "Neuromorphic Silicon Learning Machines," [Gert Cauwenberghs \(CNNAC TC Member\)](#)
- [Frontier Tech Forum: RoboUniverse, San Diego, Dec. 14, 2016.](#)
 - Invited talk, "Reverse Engineering the Cognitive Brain in Silicon," [Gert Cauwenberghs \(CNNAC TC Member\)](#)
- [Fujitsu Laboratories of America, Sunnyvale CA, Dec. 5, 2016.](#)

- Invited talk, “Advances in Unobtrusive Brain-Machine-Body Interfaces,” Gert Cauwenberghs (CNNAC TC Member)
- UCSD-UST Bilateral Symposium, San Diego CA, Nov. 18, 2016
 - Invited talk, “Towards High-Resolution Retinal Neural Interfaces,” Gert Cauwenberghs (CNNAC TC Member)

EDITORIAL SERVICE

- The IEEE Circuits and Systems Magazine, IEEE
 - Editor in Chief : Dr. Chai Wah Wu (CNNAC TC Member)
- IEEE Transactions on Circuits and Systems Part II – Express Briefs, IEEE
 - Associate Editor :
 - Ronald Tetzlaff (CNNAC TC Member)
 - Piotr Dudek (CNNAC TC Member)
- International Journal of Circuit Theory and Applications, Wiley
 - Editor in Chief : Á. Rodríguez-Vázquez
 - Editors from our TC:
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 - Fernando Corinto
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 - M. J. Ogorzalek
 - A. Csurgay
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 - Editorial Board Member form our TC: , Gert Cauwenberghs
- Microelectronics Journal, Elsevier
 - Associate Editor from our TC : Georgios Sirakoulis
- Integration, the VLSI Journal, Elsevier
 - Associate Editor from our TC : Georgios Sirakoulis
- Journal of Real-Time Image Processing, Springer
 - Associate Editor : Ricardo Carmona-Galán (CNNAC TC Member)
- Journal of Applied Mathematics, Hindawi
 - Associate Editor from our TC : Georgios Sirakoulis
- International Journal of Bifurcation and Chaos (IJBC), World Scientific
 - Honorary Editor-in-Chief : Leon Chua
 - Associate Editors :
 - Mustak E. Yalcin
 - Mattia Frasca
 - Honorary Editorial Board Members form our TC:
 - Marco Gilli
 - M. J. Ogorzalek
 - Luigi Fortuna
- SpringerBriefs in Nonlinear Circuits, Springer
 - Editors: Fortuna, Luigi,

- Members of the Editorial Board: Mattia Frasca, Ronald Tetzlaff; Mustak E. Yalcin
- **Journal of Cellular Automata, Old City Publishing House.**
 - Associate Editor from our TC : Georgios Sirakoulis
- **International Journal of Unconventional Computing, Old City Publishing House**
 - Associate Editor from our TC : Georgios Sirakoulis
- **International Journal of Parallel, Emergent and Distributed Systems, Taylor and Francis**
 - Associate Editor from our TC : Georgios Sirakoulis
- **Parallel Processing Letters, World Scientific**
 - Associate Editor from our TC : Georgios Sirakoulis
- **Recent Advances on Electrical & Electronic Engineering, Bentham**
 - Associate Editor from our TC : Georgios Sirakoulis
- **Frontiers in Neuromorphic Engineering**
 - Editorial Board Member form our TC: , Gert Cauwenberghs
- **IEEE Nanotechnology Express (ENANO), IEEE**
 - Editor-in-Chief : Wolfgang Porod (CNNAC TC Member)
- **International Journal of Neural Networks and Applications, Serials**
 - Editor in Chief : Angela Slavova (CNNAC TC Member)
- **Pliska Studia Mathematica Bulgarica , Bulgarian Academy of Sciences.**
 - Guest Chief : Angela Slavova (CNNAC TC Member)
- **Chaos, Solitons and Fractals.**
 - Editor: Mattia Frasca (CNNAC TC Member)
-

PUBLICATIONS OF THE MEMBERS

Member's publication list can be accessed by Google Scholar link. For the period of annual report, the number of publications are given in this report. Furthermore, the most related publication of each member are given.

Zarandy, Akos -(11 journal and Conf. pub) - [Google Scholar](#)

[A real-time multi-camera vision system for UAV collision warning and navigation](#)

Á Zarándy, M Nemeth, Z Nagy, A Kiss, L Santha, T Zsedrovits
Journal of Real-Time Image Processing 12 (4), 709-724

Alon, Ascoli-(11 journal and Conf. pub.)

The First Ever Real Bistable Memristors—Part II: Design and Analysis of a Local Fading Memory System

Alon Ascoli; Ronald Tetzlaff, Leon O. Chua

IEEE Transactions on Circuits and Systems II: Express Briefs (Volume: 63, Issue: 12, Dec. 2016)

Rodriguez-Vazquez, Angel-(28 journal and Conf. pub) - [Google Scholar](#)

[Mixed-signal quadratic operators for the feature extraction of neural signals](#)

M Delgado-Restituto, R Fiorelli, M Carrasco-Robles, ...

Biomedical Circuits and Systems Conference (BioCAS), 2016 IEEE, 368-371

Slavova, Angela -(2 Conf. pub)

Local Activity in Reaction-Diffusion CNN with Memristor Synapses

Angela Slavova; Galina Bobeva

CNNA 2016; 15th International Workshop on Cellular Nanoscale Networks and their Applications

Paasio, Ari -(5 journal and Conf. pub) - [Google Scholar](#)

[Ultra low-power array processor propagation circuit arrangement](#)

A Paasio

Circuits and Systems (ISCAS), 2016 IEEE International Symposium on, 2515-2518

Shi, Bertram -(13 journal and Conf. pub)- [Google Scholar](#)

[A two layer disparity selective simple cell model](#)

S Cheng, Q Peng, BE Shi

- Neural Networks (IJCNN), 2016 International Joint Conference on, 4759-4766
- Wu, Chai Wah** -(11 journal and Conf. pub) - [Google Scholar](#)
[Control localization in networks of dynamical systems connected via a weighted tree](#)
R Bapat, CW Wu
IEEE Transactions on Systems, Man, and Cybernetics: Systems
- Rekeczky, Csaba** -(1 Conf. pub)- [Google Scholar](#)
[Cellular Vision Based ADAS Applications](#)
A Horvath, I Horvath, A Kiss, D Huszar, A Palfy, L Kovacs, D Babicz, ...
CNNA 2016; 15th International Workshop on Cellular Nanoscale Networks and their Applications
- Vilariño, David L.** (1 journal pub.)
PRECISION: A Reconfigurable SIMD/MIMD Coprocessor for Computer Vision Systems-on-Chip
Alejandro Nieto; David L. Vilariño; Victor M. Brea
IEEE Transactions on Computers
Year: 2016, Volume: 65, Issue: 8
- Corinto, Fernando** -(12 journal and Conf. pub)- [Google Scholar](#)
[Memristors-Devices, Models, Circuits, Systems and Applications](#)
R Tetzlaff, F Corinto, R Picos, M Ogorzalek
International Journal of Circuit Theory and Applications 44 (7), 1478-1479
- Georgios Ch. Sirakoulis** -(52 journal and Conf. pub)- [Google Scholar](#)
[Emerging Memristor-Based Logic Circuit Design Approaches: A Review](#)
I Yourkas, GC Sirakoulis
IEEE Circuits and Systems Magazine 16 (3), 15-30
- Cauwenberghs, Gert** -(26 journal and Conf. pub, 2 Patents)- [Google Scholar](#)
[Silicon-Integrated High-Density Electrocortical Interfaces](#)
S Ha, A Akinin, J Park, C Kim, H Wang, C Maier, PP Mercier, ...
Proceedings of the IEEE 105 (1), 11-33
- András Horváth**-(3 journal and Conf. pub)- [Google Scholar](#)-
[Cellular Vision Based ADAS Applications](#)
A Horvath, I Horvath, A Kiss, D Huszar, A Palfy, L Kovacs, D Babicz, ...
CNNA 2016
- Kim, Hyongsuk** (1 conf. pub.)
Construction of CNN Template via Learning with Random Weight Change Algorithm
Shyam P. Adhikari; Changju Yang; Hyongsuk Kim; Leon O. Chua
CNNA 2016; 15th International Workshop on Cellular Nanoscale Networks and their Applications
- Suykens, Johan** -(43 journal and Conf. pub)- [Google Scholar](#)
[Robust gradient learning with applications](#)
Y Feng, Y Yang, JAK Suykens
IEEE transactions on neural networks and learning systems 27 (4), 822-835
- Vandawalle, Joos** -(7 journal and Conf. pub)- [Google Scholar](#)
[A short history of circuits and systems](#)
J Vandewalle
River Publishers
- Berni, Jorge Fernández** -(13 journal and Conf. pub, 2 patents)- [Google Scholar](#)-
[Focal-Plane Scale Space Generation with a 6T Pixel Architecture](#)
FDV Oliveira, JGRC Gomes, R Carmona-Galán, J Fernández-Berni, ...
Electronic Imaging 2016 (12), 1-8
- Nossek, Josef** -(21 journal and Conf. pub)-
Minimum BER precoding in 1-Bit massive MIMO systems
Hela Jedda; Josef A. Nossek; Amine Mezghani
2016 IEEE Sensor Array and Multichannel Signal Processing Workshop (SAM)
- Karacs, Kristof**
Saliency based Attention Mechanism for Topographic Architectures
Mihaly Radvanyi; Kristof Karacs
CNNA 2016; 15th International Workshop on Cellular Nanoscale Networks and their Applications
- Spaanenburg, Lambert** -(2 journal and Conf. pub)- [Google Scholar](#)

[Networked Neural Systems](#)

L Spaanenburg, WJ Jansen
CHIPS 2020 VOL. 2, 231-242

Chua, Leon -(12 journal and Conf. pub)-

The First Ever Real Bistable Memristors—Part II: Design and Analysis of a Local Fading Memory System

Alon Ascoli; Ronald Tetzlaff; Leon O. Chua

IEEE Transactions on Circuits and Systems II: Express Briefs

Fortuna, Luigi-(26 journal and Conf. pub)- [Google Scholar](#)

[Turing patterns in memristive cellular nonlinear networks](#)

A Buscarino, C Corradino, L Fortuna, M Frasca, LO Chua

IEEE Transactions on Circuits and Systems I: Regular Papers 63 (8), 1222-1230

Ogorzalek, Maciej-(7 journal and Conf. pub)- [Google Scholar](#)-

[Memristors-Devices, Models, Circuits, Systems and Applications](#)

R Tetzlaff, F Corinto, R Picos, M Ogorzalek

International Journal of Circuit Theory and Applications 44 (7), 1478-1479

Frasca, Mattia -(22 journal and Conf. pub)- [Google Scholar](#)-

[Turing patterns in memristive cellular nonlinear networks](#)

A Buscarino, C Corradino, L Fortuna, M Frasca, LO Chua

IEEE Transactions on Circuits and Systems I: Regular Papers 63 (8), 1222-1230

Di Marco, Mauro-(6 journal and Conf. pub)- [Google Scholar](#)

[Complete stability of feedback CNNs with dynamic memristors and second-order cells](#)

M Di Marco, M Forti, L Pancioni

International Journal of Circuit Theory and Applications 44 (11), 1959-1981

Niemier, Michael -(14 journal and Conf. pub) - [Google Scholar](#)

[Molecular cellular networks: A non von Neumann architecture for molecular electronics](#)

CS Lent, KW Henderson, SA Kandel, SA Corcelli, GL Snider, AO Orlov, ...

Rebooting Computing (ICRC), IEEE International Conference on, 1-7

Laiho, Mika --(19 journal and Conf. pub)

Large-Scale Memristive Associative Memories

Eero Lehtonen; Jussi H. Poikonen; Mika Laiho; Pentti Kanerva

IEEE Transactions on Very Large Scale Integration (VLSI) Systems

Year: 2014, Volume: 22, Issue: 3

Yalçın, Müştak E. -(2 Conf. pub)- [Google Scholar](#)

A New Implementation of Fully Programmable Complete CNN Processor Core on FPGA

V. Meric and ME Yalcin

CNNA 2016; 15th International Workshop on Cellular Nanoscale Networks and their Applications

Zoltan, Nagy-(5 journal and Conf. pub)-

Accelerating a charged single α -helix search algorithm in protein sequences using FPGA

Zoltan Nagy; Zoltan Gaspari; Akos Kovacs

CNNA 2016; 15th International Workshop on Cellular Nanoscale Networks and their Applications

Yildiz, Nerhun -(2 journal and Conf. pub)- [Google Scholar](#)

[On the way to a Third Generation Real-Time Cellular Neural Network Processor](#)

N Yildiz, E Cesur, V Tavsanoglu

CNNA 2016; 15th International Workshop on Cellular Nanoscale Networks and their Applications

Arena, Paolo -(3 journal and Conf. pub.) – [Google Scholar](#)-

[A Fly-Inspired Mushroom Bodies Model for Sensory-Motor Control Through Sequence and Subsequence Learning](#)

P Arena, M Calí, L Patané, A Portera, R Strauss

International Journal of Neural Systems 26 (06), 1650035

Dudek, Piotr -(6 journal and Conf. pub)- [Google Scholar](#)

[Parallel HDR tone mapping and auto-focus on a cellular processor array vision chip](#)

JNP Martel, LK Müller, SJ Carey, P Dudek

- Circuits and Systems (ISCAS), 2016 IEEE International Symposium on, 1430-1433
- Carmona-Galán, Ricardo** -(21 journal and Conf. pub)- [Google Scholar](#)
[Special issue on architectures of smart cameras for real-time applications](#)
R Carmona-Galán, F Berry, R Kleihorst, D Ginhac
Journal of Real-Time Image Processing 12 (4), 633-634
- Tetzlaff, Ronald** -(15 journal and Conf. pub)
Learning Deep CNN Structures
Jens Mueller; Simon Walz; Ronald Tetzlaff
CNNA 2016; 15th International Workshop on Cellular Nanoscale Networks and their Applications
- Arik, Sabri** -(4 journal paper)- [Google Scholar](#)-
[Dynamical analysis of uncertain neural networks with multiple time delays](#)
S Arik
International Journal of Systems Science 47 (3), 730-739
- Peter, Szolgsy** (4 conf . pub.)
A system concept for EMG classification from measurement to deployment
Bence J. Borbely; Peter Szolgsy
CNNA 2016; 15th International Workshop on Cellular Nanoscale Networks and their Applications
- Mladenov, Valeri** -(19 journal and Conf. pub)- [Google Scholar](#)-
[Animal call segregation using self organizing map with speeded up robust features](#)
K Pelzers, V Mladenov
Neural Networks and Applications (NEUREL), 2016 13th Symposium on, 1-5
- Tavsanoglu, Vedat** -(5journal and Conf. pub)-
Decomposition of the Nodal Conductance Matrix of a Planar Resistive Grid and Derivation of Its Eigenvalues and Eigenvectors Using the Kronecker Product and Sum With Application to CNN Image Filters
Vedat Tavşanoğlu
IEEE Transactions on Circuits and Systems I: Regular Papers
Year: 2016, Volume: 63, Issue: 12
- Sanchez, Victor M. Brea** -(6 journal and Conf. pub)-
PRECISION: A reconfigurable SIMD/MIMD coprocessor for Computer Vision Systems-on-Chip
A. Nieto, D.L. Vilariño, V.M. Brea
IEEE Transactions on Computers, vol. 65, no. 8, pp. 2548 - 2561. 2016
- Porod, Wolfgang** -(11 journal and Conf. pub)- [Google Scholar](#)
[Neural network based on parametrically-pumped oscillators](#)
G Csaba, T Ytterdal, W Porod
Electronics, Circuits and Systems (ICECS), 2016 IEEE International ...
- Cardona, Xavier Vilasis** -(56 journal and Conf. pub)- [Google Scholar](#)
[A precise measurement of the \$B^0\$ meson oscillation frequency](#)
R Aaij, CA Beteta, B Adeva, M Adinolfi, A Affolder, Z Ajaltouni, S Akar, ...
The European Physical Journal C 76 (7), 412

NOMINATIONS

The CNNAC TC endorsed;
Dr. Fernando Corinto
and
Dr. Mattia Frasca

ISCAS 2017 Track (8.6 Cellular Nanoscale Networks and Array Computing)Chair: Ricardo Carmona Galán